

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	125	348/322.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:17
S2	20	348/322.ccls. and (charge near2 detect\$4) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 10:09
S3	13	("4985758" "4768085" "4914519" "4967067" "4910597" "5262850" "5162912" "5315412" "5587814" "4910599" "4876601" "5288988" "5424529").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 14:40
S4	224	348/302.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 16:34
S5	308	348/311.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 14:53
S6	254	348/311.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:19
S7	14	348/311.ccls. and "charge detection" and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:05
S8	16	"348"/\$.ccls. and "charge detection circuit" and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:03
S9	7	348/302.ccls. and "charge detection" and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:07
S10	5	348/294.ccls. and "charge detection" and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:07
S11	345	348/294.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:12
S12	58	348/294.ccls. and CCD with (MOS CMOS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:20
S13	185	348/294.ccls. and CCD and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 13:59
S14	127	(348/294.ccls. and CCD and @ad<"20001024") not (348/294.ccls. and CCD with (MOS CMOS) and @ad<"20001024")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:20
S15	86	348/304.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 15:27
S16	132	348/302.ccls. and (CCD charge near couple\$2) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 16:40
S17	4	348/302.ccls. and (CCD charge near couple\$2) with (low near2 power) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 16:41
S18	68	"348"/\$.ccls. and (CCD charge near couple\$2) with (low near2 power) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/06 14:07
S19	29	"348"/\$.ccls. and (CCD charge near couple\$2) near10 (CMOS MOS) near10 (combin\$6 hybrid) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 17:15
S20	1	"4316205".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 17:14
S21	898	"348"/\$.ccls. and CPD and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 17:15
S22	0	"348"/\$.ccls. and CPD with (CCD and CMOS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 17:16
S23	37	"348"/\$.ccls. and CPD with (CCD CMOS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/05 17:17

S24	23	"348"/\$.ccls. and CPD with (CCD CMOS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/10/05 17:17
S25	9	"348"/\$.ccls. and ("charge priming device") with (CCD CMOS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/10/05 17:18
S26	3966	"348"/\$.ccls. and CCD near10 charge near (detect\$4 couple\$2) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/06 14:08
S27	3894	"348"/\$.ccls. and CCD near5 charge near (detect\$4 couple\$2) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/06 14:09
S28	87	"348"/\$.ccls. and CCD near5 charge near (detect\$4 couple\$2) near5 (multiple plurality) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/06 14:09
S29	3	("6515702" "5680515" "5812190").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/13 09:02
S30	1	"5449908".pn.	USPAT	OR	OFF	2004/10/20 16:55
S31	661	wadsworth.in.	USPAT	OR	OFF	2004/10/13 09:36
S32	98627	(wadsworthand mark).in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/13 09:37
S33	32	(wadsworth and mark).in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/13 09:37
S34	3	"348"/\$.ccls. and (wadsworth and mark).in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/13 09:37
S35	433797	Matsushita.as.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 16:57
S36	0	Matsushita.as. and CCd and cmos and hybrid	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 16:57
S37	41	Matsushita.as. and CCd and cmos	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 16:57
S38	15	Matsushita.as. and CCd and cmos and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:01
S39	4	Matsushita.as. and CCd with cmos and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:02
S40	1751	Matsushita.as. and CCd and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:02
S41	403	Matsushita.as. and CCd with (output (scanning near3 circuit) (charge near4 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:07
S42	40	Matsushita.as. and CCd with ((scanning near3 circuit) (charge near4 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:10
S43	345	348/294.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:10
S44	50611	348/294.ccls. ((scanning near3 circuit) (charge near4 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:11
S45	106	348/294.ccls. and ((scanning near3 circuit) (charge near4 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:11
S46	43	348/294.ccls. and ((scanning near circuit) (charge near circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:11
S47	18	348/294.ccls. and ((horizontal near scanning near circuit) (charge near circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:12
S48	431	"348"/\$.ccls. and ((horizontal near scanning near circuit) (charge near circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:12

S49	44	"348"/\$.ccls. and ((horizontal near scanning near circuit) and (charge near3 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/14 17:13
S50	18	"348"/\$.ccls. and CCD and ((horizontal near scanning near circuit) and (charge near3 circuit))and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 14:00
S51	15	sauer.in. and clamp and ((correlated near double near sampling) CDS)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 14:00
S52	13	sauer.in. and clamp and ((correlated near double near sampling) CDS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 14:38
S53	2	("5512750" "4886986").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 14:02
S54	78	"348"/\$.ccls. and clamp with ((correlated near double near sampling) CDS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 14:38
S55	27	"348"/\$.ccls. and ccd with clamp with ((correlated near double near sampling) CDS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:58
S56	7	"348"/\$.ccls. and ccd with clamp with sampl\$4 near2 hold with ((correlated near double near sampling) CDS) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:12
S57	1	"5449908".pn. and zns	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:12
S58	0	"5449908".pn. and zinc	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:12
S59	0	"5449908".pn. and sulf\$6	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:12
S60	1	"20020054227"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 15:58
S61	1	"20020054227" and drive near circuit	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:34
S62	1	"20020054227" and drive	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 16:46
S63	1	"20020054227" and (D/A A/D)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 16:49
S64	14	"348"/\$.ccls. and (A/D) and (sequential near2 comparison)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 16:50
S65	11	"348"/\$.ccls. and (A/D) and (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 16:51
S66	2	"348"/\$.ccls. and (A/D) with (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 10:31
S67	2	"348"/\$.ccls. and CCD and (A/D) with (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/15 16:56
S68	2	"348".clas. and (A/D (analog near3 digital)) with (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 10:32
S69	164	(A/D (analog near3 digital)) with (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 10:33
S70	61	"341"/\$.ccls. and (A/D (analog near3 digital)) with (sequential near2 comparison) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 10:41
S71	1	"5449908".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 10:41
S72	1	"5449908".pn. and (digital analog conver\$6)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:06

S73	6	348/311.ccls. and (A/d near10 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:40
S74	387	"348"/\$.ccls. and (A/d near5 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:40
S75	188	"348"/\$.ccls. and (A/d near3 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:41
S76	1	"348"/\$.ccls. and (multiple plurality) near3 (A/d near3 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:41
S77	1	"348"/\$.ccls. and (multiple plurality) near4 (A/d near3 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:42
S78	1	"348"/\$.ccls. and (multiple plurality) near4 (A/d near5 CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:45
S79	1	"348"/\$.ccls. and (multiple plurality) near4 (A/d near5 ("charge coupled device" CCD)) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:43
S80	25	"348"/\$.ccls. and (multiple plurality) near4 ((analog digital) A/D) near5 ("charge coupled device" CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:44
S81	8	"348"/\$.ccls. and (multiple plurality) near4 ((converter) A/D) near5 ("charge coupled device" CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:46
S82	5	"348"/\$.ccls. and (multiple plurality) near4 A/D with CCD and ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:47
S83	8	"348"/\$.ccls. and (multiple plurality) near4 ((converter) A/D) near5 ("charge coupled device" CCD) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:47
S84	5	"348"/\$.ccls. and (multiple plurality) near4 A/D with CCD and ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 11:47
S85	1	"20020054227"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:00
S86	1	"20020054227" and plurality near3 CCD	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:16
S87	1	"5449908".pn. and transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:05
S88	0	"348"/\$.ccls. and CCD near2 column near5 transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:06
S89	2	"348"/\$.ccls. and CCD near4 column near8 transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:06
S90	1	"20020054227" and "charge detection circuit"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:17
S91	1	"20020054227" and "charge detection circuit" with transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:19
S92	246	"charge detection circuit"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:19
S93	16	"charge detection circuit" near5 CCD	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:19
S94	2	"charge detection circuit" near5 CCD with transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:33
S95	513	CCD near4 transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:33
S96	99	"348"/\$.ccls. and CCD near4 transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:34

S97	84	"348"/\$.ccls. and CCD near4 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:34
S98	60	"348"/\$.ccls. and CCD near3 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 15:47
S99	3	"348"/\$.ccls. and CCD near (array column) near3 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:38
S10 0	0	348/331.ccls. and CCD near3 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:38
S10 1	4	348/311.ccls. and CCD near3 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 14:42
S10 2	4	"348"/\$.ccls. and CCD near3 output near3 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/19 15:48
S10 3	57	(A/D ADC) adj threshold	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:41
S10 4	0	(A/D ADC) adj threshold with sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:35
S10 5	381	(A/D ADC) with threshold and sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:35
S10 6	172	(A/D ADC) near5 threshold and sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:36
S10 7	33	(A/D ADC) adj threshold and sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:35
S10 8	1	(A/D ADC) near5 threshold with sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:36
S10 9	5	(A/D ADC) near9 threshold with sequential	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:45
S11 0	4	("4998265" "5113245" "5166800" "5187583").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:45
S11 1	5	("4998265" "5113245" "5166800" "5187583" "5199053").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 16:46
S11 2	0	"5449908".pn. and reset	USPAT	OR	OFF	2004/10/20 16:55
S11 3	1	"5449908".pn. and reset	USPAT	OR	OFF	2004/10/20 16:55
S11 4	2072	(A/D ADC) and successive near2 approximat\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:42
S11 5	79	"348"/\$.ccls. and (A/D ADC) and successive near2 approximat\$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:43
S11 6	55	"348"/\$.ccls. and (A/D ADC) and successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:44
S11 7	27	"348"/\$.ccls. and (A/D ADC) with successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:56
S11 8	0	"348"/\$.ccls. and (A/D ADC) with successive near2 approximat\$5 with (benefit advantage) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:44
S11 9	0	"348"/\$.ccls. and (A/D ADC) with successive near2 approximat\$5 with (benefit advantage better) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:58
S12 0	0	"348"/\$.ccls. and CCD with (A/D ADC) with successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:56
S12 1	13	"348"/\$.ccls. and CCD and (A/D ADC) with successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 08:53

S12 2	0	"348"/\$.ccls. and (A/D ADC) with successive near2 approximat\$5 with sequential and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/20 17:58
S12 3	0	348/311-324.ccls. and CCD and (A/D ADC) with successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 08:54
S12 4	173325	"348"/(311-324).ccls. and CCD and (A/D ADC) with successive near2 approximat\$5 and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 08:54
S12 5	10	348/311.ccls. and CCD and CCD near5 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:10
S12 6	10	348/311.ccls. and CCD near5 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:04
S12 7	12	348/311.ccls. and CCD near8 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:05
S12 8	2	(348/311.ccls. and CCD near8 transistor and @ad<"20001024") not (348/311.ccls. and CCD near5 transistor and @ad<"20001024")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:04
S12 9	6	348/312.ccls. and CCD near8 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:05
S13 0	2	348/313.ccls. and CCD near8 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:05
S13 1	10	348/314.ccls. and CCD near8 transistor and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:06
S13 2	18	(348/312.ccls. and CCD near8 transistor and @ad<"20001024") (348/313.ccls. and CCD near8 transistor and @ad<"20001024") (348/314.ccls. and CCD near8 transistor and @ad<"20001024")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:06
S13 3	865	"348"/\$.ccls. and transfer near2 gate and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:10
S13 4	199	"348"/\$.ccls. and CCD near6 transfer near2 gate and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:10
S13 5	181	"348"/\$.ccls. and CCD near6 transfer near gate and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:11
S13 6	32	348/311.ccls. and CCD near6 transfer near gate and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/21 09:11
S14 2	172	CCD with (sweep clear) near3 charges and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:18
S14 3	118	CCD with (sweep) near3 charges and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:18
S14 4	55	"348"/\$.ccls. and CCD with (sweep) near3 charges and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:20
S14 5	13	"348"/\$.ccls. and CCD with (extra unnecessary) near3 (sweep) near3 charges and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:44
S14 6	1065	"348"/\$.ccls. and CCD with well and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 11:44
S14 7	10	"348"/\$.ccls. and CCD with well with (charge near2 detect\$5) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 13:46
S14 8	1	"20020054227"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 13:30
S14 9	0	"20020054227" and "66"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 13:30
S15 0	62	"348"/\$.ccls. and CCD with well with ((charge near2 detect\$5) (signal near2 process\$5)) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 13:47

S15 3	18	"348"/\$.ccls. and CCD with (substrate) with ((charge near2 detect\$5) (signal near2 process\$5)) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:08
S15 4	0	"348"/\$.ccls. and CCD with (p-well n-well) with ((charge near2 detect\$5) (signal near2 process\$5)) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:29
S15 5	28	"348"/\$.ccls. and CCD with (p-well n-well) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:28
S15 6	4	"348"/\$.ccls. and CCD with deep near2 well and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:28
S15 7	62	"348"/\$.ccls. and CCD with (p-well n-well well deep) with ((charge near2 detect\$5) (signal near2 process\$5)) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:39
S15 8	28	"348"/\$.ccls. and CCD with (p-well n-well) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 14:40
S15 9	23	"348"/\$.ccls. and CCD with separat\$5 near4 well and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:21
S16 0	1	"5477070".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:54
S16 1	311	348/311.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:54
S16 2	255	348/311.ccls. and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:55
S16 3	88	348/311.ccls. and (pixel photodiode) near4 (horizontal vertical) \and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:56
S16 4	88	348/311.ccls. and (pixel photodiode) near4 (horizontal vertical) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:58
S16 5	102	348/311.ccls. and (pixel photodiode) near4 (vertical ,ear2 register) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:59
S16 6	23	348/311.ccls. and (pixel photodiode) near4 (vertical near2 register) and @ad<"20001024"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/10/29 16:59